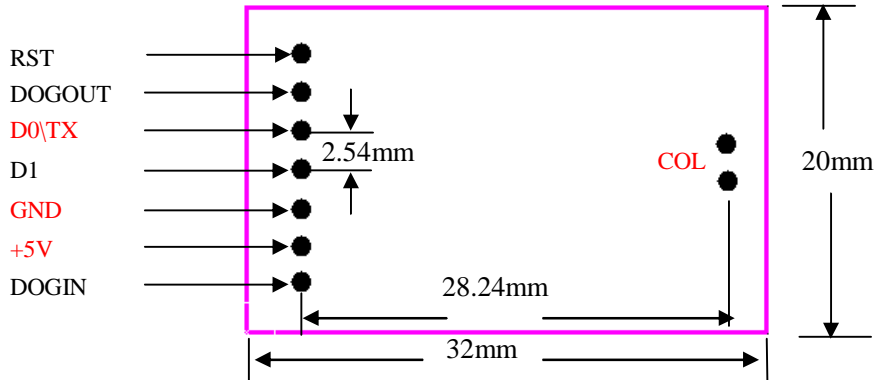


232 Reader Module(YET-125K-W)

1. Image



2. Dimensional drawing (L*W*H)



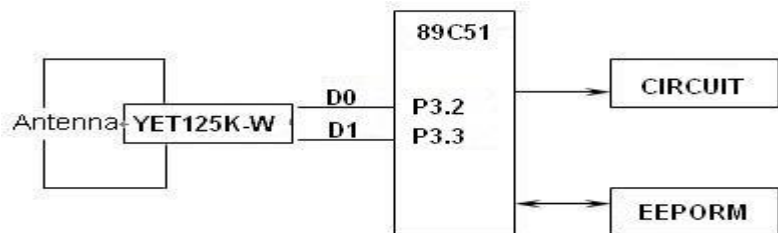
3. Explanation of pin:

- Ø COL: Connect Antenna.
- Ø GND: Ground.
- Ø D0\TX: Wiegand Data 0 output or TTL232 Output.
- Ø D1: Wiegand data 1 output or ABA clock output.
- Ø +5V: +5V Power.
- Ø DOGIN: CPU Signal input.
- Ø DOGOUT: CPU Signal output.
- Ø RST: RE-SET

4. Specification:

- Ø Antenna: L=490UH.
- Ø Working Voltage: 5V±5%.
- Ø Working Current: 40mA.
- Ø Interface: TTL232 output.
- Ø Temperature: -15 to 50 degree.

5. Typical application circuit: :



Output Format 1:

02 D1 D2 D3 D4 D5 D6 D7 D8 03

- Ø 02 : Fixed prefix 02
- Ø D1-D8: 8byte card number,ASCII code mode,Such as "1" ASCII code is "31","A" ASCII code is "41".
- Ø 03: Fixed suffix 03
- Ø Baud rate: 9600, n,8,1

Output Format 2:

Such as: F0 00 00 00 00 02 07 09 03 0B 0F 0B F9

- Ø F0 00: Prefix
- Ø 00 00 00 02 07 09 03 0B 0F: 9bits 4 half-byte 16 hex Card number is "0002793BF".
- Ø 0B: XOR checksum of "F0 to 0F".
- Ø F9: Suffix